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[SerDes Basics](#)

SerDes Basics von Texas Instruments vor 9 Jahren 3 Minuten, 27 Sekunden 32.562 Aufrufe This video describes the basics of , Serdes serializer , /deserializer technology and its benefits in the system.

[SERDES Clocking and Equalization for High-Speed Serial Links, Jack Kenney](#)

SERDES Clocking and Equalization for High-Speed Serial Links, Jack Kenney von IEEE Solid-State Circuits Society vor 1 Jahr 12 Minuten, 21 Sekunden 8.208 Aufrufe Transcript: <https://resourcecenter.sscs.ieee.org/education/confedu-ciccx-2017/SSCSCICC0051.html> Slides: ...

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Introducing LVDS Gen II/III SerDes for Industrial Applications von Texas Instruments vor 1 Jahr 7 Minuten, 7 Sekunden 998 Aufrufe LVDS Owner's Manual - 4th Edition <https://www.ti.com/lit/ug/snla187/snla187.pdf> Generation 2 and Generation 3 , LVDS SerDes , ...

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[Ground Bounce and Plane Noise](#)

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Designing Long-Reach DVI, HDMI, and PCIe von Texas Instruments vor 8 Jahren 40 Minuten 2.207 Aufrufe Bob Pease and Howard Johnson discuss challenges and solutions for driving DVI/HDMI and PCIe signals across long cables.

[The Path to 200Gbps Serial Links](#)

The Path to 200Gbps Serial Links von Alphawave IP vor 4 Monaten 29 Minuten 1.038 Aufrufe As 112Gbps PAM4 , SerDes , specifications mature 224Gbps , SerDes , will quickly start to take shape as the next evolution in , SerDes , ...

[Whiteboard Wednesdays - How the Cadence 112G SerDes IP Solves the Challenges of Long-Reach Signaling](#)

Whiteboard Wednesdays - How the Cadence 112G SerDes IP Solves the Challenges of Long-Reach Signaling von Cadence Design Systems vor 2 Jahren 8 Minuten, 10 Sekunden 6.965 Aufrufe In this week's Whiteboard Wednesday, Wendy Wu explains what NRZ and PAM-4 signaling is all about and the sources of ...